



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

10,007,840

TI-30110

Duy Q. Nguyen, et al.

Art Unit: 2661

Serial No:

Examiner:

Filed: October 30, 2001

Conf. No.: 7621

For: External Bus Arbitration Technique for Multicore DSP Device

Technology Center 2600

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Robin E. Barnum

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith. The relevancy of each of the references is noted on the enclosed Search Reports.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,



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PTO/SB/08A (08-00)

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Sheet <b>1</b> of <b>1</b>				Attorney Docket No. <b>TI-30110</b>	



U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	CA	Texas Instruments Incorporated, <i>TMS320VC5421 Fixed-Point Digital Signal Processor - Data Manual</i> , Dec., 1999, pgs. 34-35.	
	CB	Winderweedle, Bill; <i>TMS320VC5420 to TMS320VC5421 DSP Migration</i> , Dec., 1999, whole document.	
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